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ATTORNEY DOCKET NO.
AUS9200200711US1 (IBM 2601000)

PATENT APPLICATION
SERIAL NO. 10/616,850

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Dhong et al.

Serial Number: 10/616,850

Filed: July 10, 2003

For: INTEGRATED LOGIC AND LATCH
DESIGNS WITH CLOCK GATING AT
STATIC INPUT SIGNALS

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Group Art Unit: 2819

Examiner: Tan, Vibol

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

CERTIFICATE OF MAILING

I hereby certify that the original paper identified herein
is being deposited with United States Postal Service as first class
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on: _____

2/7/05

Bradley D. Ellis

Bradley D. Ellis

RESPONSE

Dear Sir:

In response to the Office Action dated November 8, 2004, ("Office Action"), Applicants
respond as follows:

02/18/2005 LWXSE1 00000001 500605 10616850

01 FC:1201 200.00 DA